

09/926461

Subclass	Class	ISSUE CLASSIFICATION

FILED UNDER 35 U.S.C. 371

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PATENT NUMBER

U.S. UTILITY PATENT APPLICATION

O.I.P.E.

PATENT DATE

m H SCANNED 8/1 Q.A. 2A

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APPLICATION NO. 09/926460	CONT/PRIOR D F	CLASS 071 340	SUBCLASS 500	ART UNIT 1754	EXAMINER Miller, Benson
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Oliver Richter 455 558 2683

APPLICANTS

Device for protecting the initial utilization of a processor /chip card

TITLE

PTO-2040
12/99**PREPARED AND APPROVED FOR ISSUE****ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket							

TERMINAL <input type="checkbox"/> DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE	
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	(Legal Instruments Examiner) _____ (Date) _____			Amount Due	Date Paid
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